MagmaDNN – High-Performance Data Analytics for Manycore GPUs and CPUs

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Dense Linear Algebra (DLA) in Applications

Dense Linear Algebra (DLA) is needed in a wide variety of science and engineering applications:

- **Linear systems:** Solve $Ax = b$
  - Computational electromagnetics, material science, applications using boundary integral equations, airflow past wings, fluid flow around ship and other offshore constructions, and many more

- **Least squares:** Find $x$ to minimize $||Ax – b||$
  - Computational statistics (e.g., linear least squares or ordinary least squares), econometrics, control theory, signal processing, curve fitting, and many more

- **Eigenproblems:** Solve $Ax = \lambda x$
  - Computational chemistry, quantum mechanics, material science, face recognition, PCA, data-mining, marketing, Google Page Rank, spectral clustering, vibrational analysis, compression, and many more

- **SVD:** $A = U \Sigma V^*$ ($Au = \sigma v$ and $A^*v = \sigma u$)
  - Information retrieval, web search, signal processing, big data analytics, low rank matrix approximation, total least squares minimization, pseudo-inverse, and many more

- **Many variations depending on structure of $A$**
  - $A$ can be symmetric, positive definite, tridiagonal, Hessenberg, banded, sparse with dense blocks, etc.

- **DLA is crucial to the development of sparse solvers**
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• DLA is crucial to the development of sparse solvers

Provided in MAGMA 2.3

**Features and Support**

- **MAGMA 2.3 FOR CUDA**
- **cIMAGMA 1.4 FOR OpenCL**
- **MAGMA MIC 1.4 FOR Intel Xeon Phi**

**Features**

- Linear system solvers
- Eigenvalue problem solvers
- Auxiliary BLAS
- Batched LA
- Sparse LA
- CPU/GPU Interface
- Multiple precision support
- Non-GPU-resident factorizations
- Multicore and multi-GPU support

**New Features**

- MAGMA Analytics/DNN
- LAPACK testing
- Linux
- Windows
- Mac OS
Why use GPUs in HPC?

**PERFORMANCE & ENERGY EFFICIENCY**

**MAGMA 2.3 LU factorization in double precision arithmetic**

- **CPU**
  - Intel Xeon E5-2650 v3 (Haswell)
  - 2x10 cores @ 2.30 GHz

- **K40**
  - NVIDIA Kepler GPU
  - 15 MP x 192 @ 0.88 GHz

- **P100**
  - NVIDIA Pascal GPU
  - 56 MP x 64 @ 1.19 GHz

- **V100**
  - NVIDIA Volta GPU
  - 80 MP x 64 @ 1.38 GHz

**Energy efficiency** (under ~ the same power draw)
What about accelerated LA for Data Analytics?

- Traditional libraries like MAGMA can be used as backend to accelerate the LA computations in data analytics applications.
- Need support for:
  1) New data layouts,
  2) Acceleration for small matrix computations,
  3) Data analytics tools.

Need data processing and analysis support for:
- Data that is multidimensional / relational.
- Small matrices, tensors, and batched computations:
  - Fixed-size batches,
  - Variable-size batches,
  - Dynamic batches,
  - Tensors.
Data Analytics and LA on many small matrices

Data Analytics and associated with it Linear Algebra on small LA problems are needed in many applications:

- Machine learning,
- Data mining,
- High-order FEM,
- Numerical LA,
- Graph analysis,
- Neuroscience,
- Astrophysics,
- Quantum chemistry,
- Multi-physics problems,
- Signal processing, etc.

Machine learning

Convolution of Filters $F_i$ (feature detection) and input image $D$:
- For every filter $F_i$ and every channel, the computation for every pixel value $O_{n,k}$ is a tensor contraction:

$$ O_{n,k} = \sum D_{n,i} F_{i,k} $$

- Plenty of parallelism; small operations that must be batched
- With data "reshape" the computation can be transformed into a batched GEMM (for efficiency; among other approaches)

Applications using high-order FEM

- Matrix-free basis evaluation needs efficient tensor contractions,

$$ C_{i1,i2,i3} = \sum_k A_{k,i1} B_{k,i2,i3} $$

- Within ECP CEED Project, designed MAGMA batched methods to split the computation in many small high-intensity GEMMs, grouped together (batched) for efficient execution:

Batch_{ $C_{i3} = A^T B_{i3}$, for range of $i3$ }
MagmaDNN – Data Analytics Tool

- **MagmaDNN 0.1-Alpha – HP Data analytics and ML**
  GPU-accelerated numerical software using MAGMA as computational backend to accelerate its LA computations

- **Open source; looking for feedback and contributions**
  Started with students from REU/RECSEM program
  [https://bitbucket.org/icl/magmadnn](https://bitbucket.org/icl/magmadnn)

- **Implemented/proposed so far**
  - Tensors and tensor operations
  - Deep learning primitives:
    - Fully-connected layers, convolutional layers, pooling layers, activation layers, and output layers.
    - All of them support SGD back-propagation training
  - Established adapters for calling CuDNN
  - Applied MagmaDNN to the MNIST benchmark using multilayer perceptron or a convolutional neural network.

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- **FEATURES AND SUPPORT**
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  - MAGMA MIC 1.4 for Intel Xeon Phi

- **Linpack**
  - Linear system solvers
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[http://icl.cs.utk.edu/magma](http://icl.cs.utk.edu/magma)  [https://bitbucket.org/icl/magmadnn](https://bitbucket.org/icl/magmadnn)
**Fully connected layers**

**Data** (input, output, NN weights, etc.) is handled through **tensor abstractions**

// 2d tensor for n_images and n_features in the corresponding dimensions
Tensor<float> Images = Tensor<float>({n_images, n_features});

**Support for various layers:**
Fully connected (FCLayer), convolution, activation, flatten, pooling, input, output, etc. layers

// Create layers for the network
FCLayer<float> *FC1 = new FCLayer<float>(&inputLayer, 128);
ActivationLayer<float> *actv1 = new ActivationLayer<float>(FC1, SIGMOID);
FCLayer<float> *FC2 = new FCLayer<float>(actv1, n_output_classes);

**Support networks – composed of layers**
std::vector<Layer<float>*> vec_layer;
vec_layer.push_back(&inputLayer);
vec_layer.push_back(FC1);
vec_layer.push_back(actv1);
vec_layer.push_back(FC2);
...
Convolutional network layers

- Layers are typically 3D volumes
- Handled through tensors
- Each layer transforms 3D tensor to 3D tensor
- Layers support the forward and backward pass algorithms for the training
- Support for optimization solvers (GD and derivatives)
  - Gradient Descent (GD)
  - Stochastic Gradient Descent (SGD)
  - Mini-Batch Gradient Descent (MB-GD)
How to accelerate on manycore GPU and CPUs?

- Convolutions can be accelerated in various ways:
  - Unfold and GEMM
  - FFT
  - Winograd minimal filtering – reduction to batched GEMMs

- Use autotuning to handle complexity of tuning

Convolutions can be accelerated in various ways:

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<th>$k$</th>
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<td>512</td>
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</tbody>
</table>

Require matrix-matrix products of various sizes, including batched GEMMs
How to implement fast batched DLA?

Problem sizes influence algorithms & optimization techniques

Kernels are designed various scenarios and parameterized for autotuning framework to find “best” performing kernels

Optimizing GEMM’s: Kernel design

- Reading/writing the elements is based on the TB size (# threads) and so is an extra parameter.
- Also it could be different for A, B and C
Examples

- MagmaDNN has testing/example drivers
- Example implementing the MNIST benchmark using MagmaDNN multilayer perceptron or a convolutional neural network

The MNIST benchmark is a NN for recognizing handwritten numbers
Input for the training are images of handwritten numbers and the labels indicating what are the numbers
Examples ...

EEG-Based Control of a Computer Cursor Movement with Machine Learning. Part B
Students: Justin Klimarx (University of Tennessee), David Seaff (Loyola University), Lucien Ng (The Chinese University of Hong Kong)
Mentors: Xiaopeng Zhao (UTK), Stanimire Tomov (UTK), Kwai Wong (UTK)

Introduction

Brain-Computer Interface (BCI) systems have become a source of great interest in the recent years. Establishing a link with the brain will lead to many possibilities in the healthcare, robotics, or entertainment fields.

Instead of using invasive BCI, we are trying to understand user intention by classifying their Electroencephalography (EEG) result, which recorded electrical activities of the users' brain, with state-of-art machine learning technologies. Through this technique, more advanced prosthetic devices can be developed and handicapped patients can be benefited from it.

Objectives

- To classify the user intending cursor movement by using EEG signal with high accuracy, and
- To accelerate the process to acceptable speed

Unmixing 4-D Psychographic Image: Part B: Data Approach
Student: Zhen Zhang(CUHK), Huanlin Zhou(CUHK), Michaela D. Shoffner(UTK)
Mentors: R. Archibald(ORNL), S. Tomov(UTK), A. Haidar(UTK), K. Wong(UTK)

INTRODUCTION

There are three known basic modes, $M_0, M_1, M_2$, each of which is a 2688 by 2688 image. The problem is, for each input image, we try to find a representation of $I$ using the three basic modes. It is known that the input image can be closely represented as a linear combination of the three basic modes, namely,

$$ I = \alpha M_0 + \beta M_1 + \gamma M_2 $$

The problem can easily be solved by least square method. However, the result of least square is quite far away from what we desire. For example, for one of the input images, where the true coefficients are $(\alpha, \beta, \gamma) = (1, 1, 1)$, the output of least square method is $(0.9950, 0.8284, 0.7945)$. For $(\alpha, \beta, \gamma) = (1, -1, -1)$, the result of least square is $(0.9426, -0.3582, -0.3590)$, which has large notable error.

A machine learning method with interpolation is proposed to achieve better accuracy for current data. For example, for an image with $(\alpha, \beta, \gamma) = (1, -1, -1)$, the output of the neural network is $(0.9994, -0.9675, -0.9828)$, with 2 hidden layers, 15 nodes in each hidden layer and regularisation parameter $= 0.01$.

generate synthetic data with interpolation. For each of the pixels in an input image, we know the bias of linear approximation. It is assumed that the bias is a result of mutual effect of $\beta$ and $\gamma$. Namely, the bias for a pixel $(x, y)$ can be written as following:

$$ B = B_{x,y}(\beta, \gamma) $$

We can interpolate the bias using the four points for each pixel. If we take $M_1$ and $M_2$ also as input images, we can interpolate using six points.

COMPUTATIONS & RESULTS

To simplify the inputs we sum up all pixel in a 192 by 192 block in an input image or basic mode; we will only consider the 14 by 14 summed image.

A better testing of the code check if the output is $(1.0000, 0.0000, 0.0000)$ which means
Current work and Future directions

• Performance portability and unified support on GPUs/CPUs
  – C++ templates w/ polymorphic approach;
  – Parallel programming model based on CUDA, OpenMP task scheduling, and MAGMA APIs.

• Autotuning
  – Critical for performance to provide tuning that is application-specific;
  – A lot of work has been done (on certain BLAS kernels and the approach) but still need a simple framework to handle the entire library.

• Extend functionality, kernel designs, and algorithmic variants
  – BLAS, Batched BLAS, architecture and energy-aware
  – New algorithms and building blocks, architecture and energy-aware
  – Randomization algorithms, e.g., for low-rank approximations, and applications

• Use and integration with applications of interest (with ORNL collaborators)
  – Brain-computer interface systems
  – Post-processing data from electron detectors for high-resolution microscopy studies (Unmixing 4-D Ptychographic Images)
  – Optimal cancer treatment strategies
Collaborators and Support

MAGMA team
http://icl.cs.utk.edu/magma

PLASMA team
http://icl.cs.utk.edu/plasma

Collaborating partners
University of Tennessee, Knoxville
Lawrence Livermore National Laboratory
University of California, Berkeley
University of Colorado, Denver
INRIA, France (StarPU team)
KAUST, Saudi Arabia