## NOVEMBER 2021

<table>
<thead>
<tr>
<th>Rank</th>
<th>System</th>
<th>Site</th>
<th>Country</th>
<th>Cores</th>
<th>GFLOPS/P</th>
<th>TFLOPS/MW</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Fugaku</td>
<td>RIKEN R-CCS</td>
<td>Japan</td>
<td>7,630,848</td>
<td>442.0</td>
<td>29.9</td>
</tr>
<tr>
<td>2</td>
<td>Summit</td>
<td>DOE/SC/ORNL</td>
<td>USA</td>
<td>2,414,592</td>
<td>148.6</td>
<td>10.1</td>
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<td>3</td>
<td>Sierra</td>
<td>DOE/NNSA/LLNL</td>
<td>USA</td>
<td>1,572,480</td>
<td>94.6</td>
<td>7.44</td>
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<tr>
<td>4</td>
<td>Sunway TaihuLight</td>
<td>NSCC in Wuxi</td>
<td>China</td>
<td>10,649,600</td>
<td>93.0</td>
<td>15.4</td>
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<td>5</td>
<td>Perlmutter</td>
<td>LBNL</td>
<td>USA</td>
<td>761,856</td>
<td>70.9</td>
<td>2.58</td>
</tr>
</tbody>
</table>

### Performance Development

- **SUM**
- **N=1**
- **N=500**

### Architectures

- SIMD
- MPP
- Constellations
- Clusters
- SMP

### Chip Technology

- Alpha
- IBM
- HP
- MIPS
- ARM
- IBM
- AMD
- Intel

### Installation Type

- Vendor
- Research
- Industry
- Academic
- Government

### Accelerators/Co-processors

- NVIDIA
- AMD
- Intel
- Energy Consumption/Watt
- Energy Efficiency/RMIPS

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**HPLINPACK**

A Portable Implementation of the High Performance Linpack Benchmark for Distributed Memory Computers


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